

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): SUZUKI et al.	Atty. Dkt.: 01-528
Serial No.: Unknown	Group Art Unit:
Filed: Concurrently herewith	Examiner:
Title: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME	

Commissioner for Patents
Arlington, VA 22202

Date: December 9, 2003

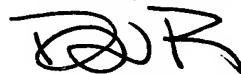
INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, the reference(s) listed on the attached Form PTO-1449 is/are submitted for consideration by the Examiner without any admission that it/they constitute(s) statutory prior art, or without any admission that it/they contain(s) subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

The Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed reference(s) has/have been considered and made of record.

Respectfully submitted,



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FORM PTO-1449		ATTY. DKT NO.	01-528	SER. NO.
		APPLICANT	SUZUKI et al.	
		FILING DATE	December 9, 2003	GROUP

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
		6,525,375	Feb. 25, 2003	Yamaguchi et al.		
		2003/0141514	Jul. 31, 2003	Yamaguchi et al.		
		6,118,149	Sep. 12, 2000	Nakagawa et al.		
		6,542,231	Sep. 17, 2002	Nakagawa et al.		

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS	TRANSLATION	YES	NO
		JP-A-2001-127287	5/11/01	JAPAN				X (Abstract)		

* Full English text is available in machine-translated form in JPO (Japanese Patent Office) English language web site at <http://www1.ipdl.jpo.go.jp/PA1/cgi-bin/PA1INDEX>.

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

EXAMINER		DATE CONSIDERED